FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT

Applicant(s): Thaddeus John Gabara

Case:

Serial No.: Filing Date: Group:

TBA 09/870, May 30, 2001

TBA 2816



| | | | | FILING DATE |
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| DOCUMENT NO. | DATE | NAME | CLASS/SUBCLASS | IF APPROPRIATE |
| . 09/162,852 | 9/29/98 | T.J. Gabara and S.A. Muj | taba, "Multi-Input Compa | rator." |
| . 6,191,623 | 02/20/01 | Gabara | 327/71 | 09/29/98 |
| . 6,097,768 | 08/01/00 | Janesch et al. | 375/80 | 1412/97 |
| . 5,877,718 | 03/02/99 | Andoh et al. | 341/155 | 03/24/97 |
| . 5,790,063 | 08/04/98 | Koifman et al. | 341/155 | 12/16/96 |
| . 5,636,252 | 06/03/97 | Patel et al. | 375/345 | 12/15/98 |
| . 5,606,625 | 02/25/97 | Dallavalle et al. | 384/107 | 05/26/94 |
| . 5,576,837 | 11/19/96 | Strolle et al. | 386/33 | 08/26/94 |
| . 5,563,531 | 10/08/96 | Meyer | 327/3 | 09/15/94 |
| 0. 4,767,949 | 08/30/88 | Mayweather, III | 327/80 | 05/01/87 |
| | FOR | EIGN PATENT DOCUME | ENTS | |
| DOCUMENT NO. | DATE | COUNTRY | CLASS/SUBCLASS | TRANSLATION YES NO |
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| | | OTHER DOCUMENTS | | |
| | | rooms up sur a 4 cinc parci | | |
| REF NO. AUT | HOR, TITLE, DATE, P | ERTINENT PAGES, ETC. | | |
| . J. Montanaro et tate Circuits, Vol | al., "A 160-MF . 31, No. 11, pp | Hz, 32-b, 0.5-W CMOS RISC . 1703-1714, 1996. | C Microprocessor," IEEE J | ournal of Solid- |
| . JC. Kim et al., | , "CMOS Sense | Amplifier-Based Flip-Flop | with Two N-C2MOS Outpo | ıt Latches," |
| | . 09/162,852 . 6,191,623 . 6,097,768 . 5,877,718 . 5,790,063 . 5,636,252 . 5,606,625 . 5,576,837 . 5,563,531 0. 4,767,949 | . 09/162,852 9/29/98 . 6,191,623 02/20/01 . 6,097,768 08/01/00 . 5,877,718 03/02/99 . 5,790,063 08/04/98 . 5,636,252 06/03/97 . 5,606,625 02/25/97 . 5,576,837 11/19/96 . 5,563,531 10/08/96 0. 4,767,949 08/30/88 FOR DOCUMENT NO. DATE REF NO. AUTHOR, TITLE, DATE, P J. Montanaro et al., "A 160-MF tate Circuits, Vol. 31, No. 11, pp JC. Kim et al., "CMOS Sense | . 09/162,852 9/29/98 T.J. Gabara and S.A. Muj . 6,191,623 02/20/01 Gabara . 6,097,768 08/01/00 Janesch et al 5,877,718 03/02/99 Andoh et al 5,790,063 08/04/98 Koifman et al 5,636,252 06/03/97 Patel et al 5,606,625 02/25/97 Dallavalle et al 5,576,837 11/19/96 Strolle et al 5,563,531 10/08/96 Meyer 0. 4,767,949 08/30/88 Mayweather, III FOREIGN PATENT DOCUME DOCUMENT NO. DATE COUNTRY OTHER DOCUMENTS REF NO. AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC. J. Montanaro et al., "A 160-MHz, 32-b, 0.5-W CMOS RISC tate Circuits, Vol. 31, No. 11, pp. 1703-1714, 1996. | O9/162,852 9/29/98 T.J. Gabara and S.A. Mujtaba, "Multi-Input Compara |

U.S. PATENT DOCUMENTS

* Note: Not considered because undated

3. "MOS Operational Amplifiers," Chapter Four, pp. 121-126, undated.

Examiner HAI L. NGUYEN Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.